

Attorney's Docket No. SONY-P7325

SIR:

Transmitted herewith for filing is the patent application of Inventor(s): YASUO IWASAKI

Title: AUDIO AND/OR VIDEO DATA RECORDING AND REPRODUCING APPARATUS AND METHOD OF SAME

CERTIFICATION UNDER 37 CFR § 1.10

I hereby certify that this New Application and the documents referred to as enclosed herein are being deposited with the United States Postal Service on this date April 7, 1997, in an envelope bearing "Express Mail Post Office To Addressee" Mailing Label Number EM393652036US addressed to: Box Patent Application, Assistant Commissioner of Patents, Washington, D.C. 20231.

HOWARD WONG

(Name of person mailing paper)

(Signature)

Enclosed are:

1. The papers required for filing date under CFR § 1.53(b):
50 Pages of specification (including claims); 11 Sheets of drawings.
 formal
 x informal
2. x Declaration or oath (unsigned)
3. x Power of Attorney (unsigned)
4. Assignment of the invention to Sony Corporation (including Form PTO-1595).
5. Fee Calculation
 Amendment changing number of claims or deleting multiple dependencies is enclosed.

CLAIMS AS FILED

	Number Filed	Number Extra	Rate	Basic Fee (\$770)
Total Claims	9 - 20	0	x \$22.00	0
Independent Claims	2 - 3	0	x \$80.00	0
— Multiple dependent claim(s), if any			\$260.00	0

*If less than zero, enter "0".

Filing Fee Calculation \$770.00

6. ☐ Small Entity Statement - verified statement enclosed

50% Filing Fee Reduction (if applicable) \$0

7. Other Fees

☐ Recording Assignment [\$40.00] \$0

☐ Other fees

☐ Specify _____ \$0

Total Fees Enclosed \$770.00

8. Payment of Fees

☒ Check(s) in the amount of \$ 770.00 enclosed.

☐ Charge Account No. 12-1420 in the amount of \$__.

A duplicate of this transmittal is attached.

9. ☒ Authorization to Charge Additional Fees

The Commissioner is hereby authorized to charge any additional fees (or credit any overpayment) associated with this communication and which may be required under 37 CFR § 1.16 or § 1.17 to Account No. 12-1420. **A duplicate sheet is attached.**

10. All correspondence regarding this application should be forwarded to the undersigned attorney:

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Facsimile: 415/433-8716

11. ☐ Information Disclosure Statement

12. ☒ Return Receipt Postcard

13. ☒ Claim of priority in foreign application

14. ☒ Certified copy of Japanese Patent
Appln. No. P08-085102 f. 4/08/96

LIMBACH & LIMBACH L.L.P.

April 7, 1997

(Date)

(S97P325US00)

By: _____

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NEW APPLICATION TRANSMITTAL

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HOWARD WONG
(Name of person mailing paper)


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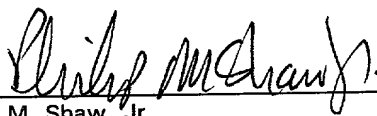
14. ☒ Certified copy of Japanese Patent
Appln. No. P08-085102 f. 4/08/96

LIMBACH & LIMBACH L.L.P.

April 7, 1997
(Date)

(S97P325US00)

By: _____


Philip M. Shaw, Jr.
Registration No. 25,376
Attorney(s) or Agent(s) of Record

AUDIO AND/OR VIDEO DATA RECORDING
AND REPRODUCING APPARATUS AND METHOD OF SAME

5 BACKGROUND OF THE INVENTION

1. Field of the Invention

The present invention relates to an audio
and/or video data recording and reproducing apparatus for
recording audio and/or video data in a recording apparatus
10 capable of performing random access such as a hard disk
drive and for reproducing the recorded audio and/or video
data and to a method of the same.

2. Description of the Related Art

Recording apparatuses (hard disk array
15 apparatuses) comprised of a plurality of hard disk drives
which are connected in parallel, able to be randomly
accessed, and in addition having a very large recording
capacity have recently been commercialized.

To edit audio data and video data or one of the
20 same (audio and/or video data) of a digital format, it is
convenient to record the audio and/or video data in the
above hard disk array apparatus and reproduce the desired
part of the audio and/or video data for the editing
rather than record the audio and/or video data on a tape
25 recording medium in which substantially only sequential

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access is possible.

However, the data rate of the audio and/or video data is usually high, i.e., about 30 Mbps, and therefore in order to record the audio and/or video data at a high speed and reproduce a desired part of the recorded audio and/or video data, it is necessary to make the access time to the recording medium as short as possible to perform the recording and reproduction with a high efficiency.

Further, for example, when engaging in complex editing such as connecting a large number of pieces of audio and/or video data to produce audio and/or video data for a long program, it is necessary to connect a plurality of recording apparatuses for the audio and/or video data. In order to connect a plurality of recording apparatuses for audio and/or video data, many cables become necessary, the arrangement of the apparatuses becomes difficult, and much cost is incurred. Further, the control of the recording and reproducing operation for each of the plurality of audio and/or video data is difficult, so high efficiency editing is difficult.

SUMMARY OF THE INVENTION

An object of the present invention is to provide an audio and/or video data recording and reproducing

apparatus capable of recording audio and/or video data in
a recording apparatus to which random access is possible
and of reproducing a desired part of the recorded audio
and/or video data which is suitable for use for editing
5 and a method of the same.

Further, another object of the present invention is
to provide an audio and/or video data recording and
reproducing apparatus with which the access time when
recording and reproducing the audio and/or video data
10 with respect to the recording medium to which random
access is possible is made as shorter as possible and the
audio and/or video data can be recorded and reproduced
with a high efficiency and a method of same.

Further, still another object of the present
15 invention is to provide an audio and/or video data
recording and reproducing apparatus with which complex
editing with respect to the audio and/or video data can
be carried out with a high efficiency by connecting a
plurality of apparatuses and in addition the physical
20 connections such as cable connections can be reduced even
in a case where a plurality of apparatuses are connected
and a method of same.

To attain the above objects, the audio and/or video
data recording and reproducing apparatus according to the
25 present invention has a receiving means for receiving a

data stream in which a plurality of audio data and video data or one of the same (audio and/or video data) are multiplexed in a predetermined order; a demultiplexing means for demultiplexing each of the audio and/or video data from the received data stream; a plurality of recording means for recording each of the demultiplexed plurality of audio and/or video data so that random access is possible; a reproducing means for reproducing the plurality of audio and/or video data respectively recorded in the plurality of recording means; and a multiplexing means for multiplexing the reproduced plurality of audio and/or video data in the predetermined order and generating the data stream.

Preferably, each of the plurality of recording means adopts a mirror configuration having a plurality of recording apparatuses for recording the same audio and/or video data.

Preferably, each of the plurality of recording means adopts an array configuration in which a plurality of recording apparatuses are connected in parallel.

Further, for example, this apparatus is constituted so that each of the plurality of recording means has two hard disk drives having the same configuration and so that a plurality of channels worth of audio and/or video data demultiplexed by the demultiplexing means are

recorded in two recording apparatuses in parallel while performing back-up so that the recorded audio and/or video data will not be lost even in a case where a recording apparatus breaks down, thereby improving the reliability.

Further, the recording means used for the hard disk array apparatus, in which for example each of a plurality of recording means is constituted by a plurality of hard disks, is made hierarchical so that the recording capacity of the audio and/or video data is made larger.

The reproducing means reproduces the audio and/or video data recorded on the hard disks by the recording means.

The multiplexing means multiplexes the plurality of audio and/or video data reproduced by the reproducing means in the original order corresponding to the plurality of channels to generate a data stream complying with the SMPTE (Society of Motion Picture and Television Engineers) -259M and transmits the same.

Further, for example, this apparatus is constituted so that the control data for instructing the contents of the recording and reproduction processing by the editing apparatus is multiplexed in the data stream and supplied to a plurality of audio and/or video data recording and reproducing apparatuses according to the present

invention. The recording means and reproducing means of each audio and/or video data recording and reproducing apparatus operate in synchronization with a synchronization signal in the data stream, thereby to

5 enable the transmission of the audio and/or video data and the control data by one transmission path with the editing apparatus to reduce the amount of cables, enable the control of the plurality of audio and/or video data recording and reproducing apparatuses from the editing apparatus, and further synchronize the audio and/or video data to be recorded and reproduced by each audio and/or video data recording and reproducing apparatus.

Further, the audio and/or video data recording and reproduction method according to the present invention
15 comprises the steps of receiving a data stream in which a plurality of audio data and video data or one of the same (audio and/or video data) are multiplexed in a predetermined order; demultiplexing each of the audio and/or video data from the received data stream;
20 recording each of the demultiplexed plurality of audio and/or video data so that random access is possible; reproducing the plurality of audio and/or video data respectively recorded in the plurality of recording means; and multiplexing the reproduced plurality of audio
25 and/or video data in the predetermined order and

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generating the data stream.

BRIEF DESCRIPTION OF THE DRAWINGS

These and other objects and features of the present
5 invention will become clearer from the following
description of the preferred embodiments with reference
to the attached drawings, in which:

Fig. 1 is a view of the configuration of an audio
and/or video editing system according to a first
10 embodiment of the present invention;

Fig. 2 is a view of the configuration of an audio
and/or video data recording and reproducing apparatus
shown in Fig. 1;

Fig. 3 is a view of the configuration of an audio
15 editing system according to a second embodiment of the
present invention;

Fig. 4 is a view of the configuration of an audio
data recording and reproducing apparatus shown in Fig. 3;

Fig. 5 is a view of an access timing when a hard
20 disk array reproduces the audio and/or video data from a
hard disk drive in the audio and/or video data editing
system (Fig. 1) shown in the first embodiment;

Fig. 6 is a view of the configuration of the audio
and/or video editing system in a third embodiment;

25 Fig. 7 is a schematic view of the configuration and

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operation of the audio and/or video data recording and reproducing apparatus shown in Fig. 6;

Fig. 8 is a view of a detailed configuration of the audio and/or video data recording and reproducing apparatus shown in Fig. 6;

Fig. 9 is a view of the configuration of a data stream transmitted between the audio and/or video editing apparatus and the audio and/or video data recording and reproducing apparatus in the audio and/or video editing system shown in Fig. 7;

Figs. 10A to 10D are views of a data access operation of the audio and/or video data recording and reproducing apparatus shown in Fig. 6; and

Fig. 11 is a view of the configuration of the audio and/or video editing system according to a fourth embodiment of the present invention.

DESCRIPTION OF THE PREFERRED EMBODIMENTS

First embodiment

Below, an explanation will be made of a first embodiment of the present invention.

Figure 1 is a view of the configuration of an audio and/or video editing system 3 according to a first embodiment of the present invention.

As shown in Fig. 1, the audio and/or video editing

system 3 is constituted by an audio and/or video data recording and reproducing apparatus 1, small computer system interface circuits (SCSI circuits) 30 and 32, and an audio and/or video editing apparatus (AV editing apparatus) 34.

Figure 2 is a view of the configuration of the audio and/or video data recording and reproducing apparatus 1 shown in Fig. 1.

As shown in Fig. 1 and Fig. 2, the audio and/or video data recording and reproducing apparatus 1 is constituted by a disk array controller 10 and a hard disk array 12. The hard disk array 12 is constituted by for example nine hard disk drives (HDD0 to HDD8) 120a to 120i.

In the audio and/or video editing system 3 (Fig. 1), the audio and/or video editing apparatus 34 controls the audio and/or video data recording and reproducing apparatus 1 via a control interface (not illustrated), makes the audio and/or video data recording and reproducing apparatus 1 reproduce the audio data and video data or one of the same (audio and/or video data), receives this via the SCSI circuits 30 and 32, and performs the editing, outputs the audio and/or video data after the editing to the audio and/or video data recording and reproducing apparatus 1, and makes the same

record the audio and/or video data.

In the audio and/or video data recording and reproducing apparatus 1 (Fig. 2), the disk array controller 10 adds parity data P_n (n is an integer) to data blocks 0, 1, ... of the audio and/or video data input from the audio and/or video editing apparatus 34 under the control of the audio and/or video editing apparatus 34 via the control interface, distributes the audio and/or video data to the hard disk drives 120a to 120h of the hard disk array 12, makes them record the audio and/or video data, and makes the hard disk drive 120i record the parity data P_n .

Further, the disk array controller 10 makes the hard disk drives 120a to 120i reproduce the data blocks of the audio and/or video data and the parity data thereof for which the reproduction is requested by the audio and/or video editing apparatus 34 via the control interface, performs the parity check, and outputs the same to the audio and/or video editing apparatus 34 via the SCSI circuits 30 and 32.

First, an explanation will be made of the operation when the audio and/or video data are recorded in the audio and/or video data recording and reproducing apparatus 1 at the audio and/or video editing system 3.

The audio and/or video editing apparatus 34 controls

the audio and/or video data recording and reproducing apparatus 1 via the control interface and makes this start the recording operation. Further, the audio and/or video editing apparatus 34 sequentially outputs for example the data blocks 0, 1, ..., 15 of the audio and/or video data which have been already edited to the audio and/or video data recording and reproducing apparatus 1 via the SCSI circuits 30 and 32.

The disk array controller 10 of the audio and/or video data recording and reproducing apparatus 1 sequentially generates the parity data Pn with respect to each of the input data blocks 0, 1, ..., 15 of the audio and/or video data as shown in Fig. 2, distributes the data blocks 0 and 8 among the data blocks 0, 1, ..., 15 to the hard disk drive 120a, distributes data blocks 1 and 9 to a hard disk drive 120b, distributes data blocks 2 and 10 to a hard disk drive 120c, distributes data blocks 3 and 11 to a hard disk drive 120d, distributes data blocks 4 and 12 to a hard disk drive 120e, distributes data blocks 5 and 13 to a hard disk drive 120f, distributes data blocks 6 and 14 to a hard disk drive 120g, distributes data blocks 7 and 15 to a hard disk drive 120h, and makes these hard disk drives record the same and makes the hard disk drive 120i record the generated parity data.

Next, an explanation will be made of the operation when the audio and/or video data are reproduced from the audio and/or video data recording and reproducing apparatus 1 in the audio and/or video editing system 3.

5 The audio and/or video editing apparatus 34 controls the audio and/or video data recording and reproducing apparatus 1 via the control interface and makes this start for example the reproduction operation of audio and/or video data.

10 The disk array controller 10 of the audio and/or video data recording and reproducing apparatus 1 controls the hard disk drives 120a and 120i and makes them sequentially reproduce the data blocks of the audio and/or video data and the parity data Pn thereof.

15 Further, the disk array controller 10 performs a parity check with respect to the audio and/or video data by using the reproduced parity bit and outputs the same to the audio and/or video editing apparatus 34 via the SCSI circuits 30 and 32.

20 Note that, in actuality, the transfer data rate for each of the hard disk drives 120a to 120h is about 24 Mbps and the data rate of the audio and/or video data is 30 Mbps. In the audio and/or video editing system 3, the transfer of a maximum of six channels of the audio and/or
25 video data $[6 \times 192 \text{ Mbps} (= 24 \text{ Mbps} \times 8)/30 \text{ Mbps}]$ is

possible between the audio and/or video editing apparatus 34 and the audio and/or video data recording and reproducing apparatus 1.

As explained above, in the audio and/or video editing system 3, the audio and/or video editing apparatus 34 performs the recording and reproduction of the audio and/or video data with respect to the hard disk array 12 of the audio and/or video data recording and reproducing apparatus 1 comprised of the hard disk drives connected in parallel. Accordingly, the editor can reproduce any desired part of the audio and/or video data by performing a random access to the hard disk array 12 by using the audio and/or video editing apparatus 34 and use the same for the editing and thereby perform the editing work with a high efficiency.

Further, as mentioned above, the transfer of a plurality of audio and/or video data is possible between the audio and/or video editing apparatus 34 and the audio and/or video data recording and reproducing apparatus 1, therefore the audio and/or video editing apparatus 34 can perform the editing by making the audio and/or video data recording and reproducing apparatus 1 supply a plurality of audio and/or video data and further can make the audio and/or video data recording and reproducing apparatus 1 record the already edited audio and/or video data therein.

Further, the hard disk array 12 is constituted by a plurality of hard disk drives and has a very large storage capacity (several gigabytes to several tens of gigabytes). Accordingly, the audio and/or video editing system 3 is preferred for connecting a large number of pieces of audio and/or video data to produce audio and/or video data of a long program.

Further, the hard disk array 12 of the audio and/or video data recording and reproducing apparatus 1 has the hard disk drives 120a to 120h for recording the audio and/or video data proper and the hard disk drive 120i for recording the parity data Pn. Accordingly, the hard disk array 12 of the audio and/or video data recording and reproducing apparatus 1 is preferred for the recording of the audio and/or video data to which parity is added and redundancy is given and so the reliability when recording and reproducing the audio and/or video data is high.

Note that it is also possible to constitute the audio and/or video data recording and reproducing apparatus 1 by using another recording and reproducing apparatus using a recording medium capable of recording and reproducing audio and/or video data so that random access is possible in place of the hard disk drives 120a to 120i, for example, a magneto-optical disk.

Further, it is also possible to use the audio and/or

video editing system 3 for the editing of only one of the audio data and video data.

Second embodiment

Below, an explanation will be made of a second
5 embodiment of the present invention.

Figure 3 is a view of the configuration of an audio editing system 4 according to a second embodiment of the present invention.

As shown in Fig. 3, the audio editing system 4
10 according to the present invention is constituted by an audio editing apparatus 44, SCSI circuits 40a to 40d and 42a to 42d, and audio data recording and reproducing apparatuses 2a to 2d.

Figure 4 is a view of the configuration of the audio
15 data recording and reproducing apparatuses 2a to 2d shown in Fig. 3.

As shown in Fig. 4, each of the audio data recording and reproducing apparatuses 2a to 2d is constituted by a disk array controller 20 and a disk device 22 having a
20 mirror configuration. The disk device 22 adopts a mirror configuration comprising the same hard disk drive 220a (data drive) and hard disk drive 220b (mirror drive).

In the audio editing system 4 (Fig. 3), the audio editing apparatus 44 controls for example four audio data
25 recording and reproducing apparatuses 2a to 2d via a

control interface (not illustrated), makes each of the audio data recording and reproducing apparatuses 2a to 2d reproduce the audio data, receives the same via the SCSI circuits 40a to 40d and 42a to 42d, performs the editing with respect to four channels worth of audio data in parallel, outputs the audio data after the editing to each of the audio data recording and reproducing apparatuses 2a to 2d, and makes them record the audio data.

In each of the audio data recording and reproducing apparatuses 2a to 2d (Fig. 4), the disk array controller 20 makes the hard disk drives 220a and 220b record the data blocks 0, 1, ... of the audio data input from the audio editing apparatus 44 under the control of the audio editing apparatus 44 via the control interface. That is, the same audio data is recorded in the hard disk drives 220a and 220b.

Further, the disk array controller 20 makes the hard disk drive 220a reproduce the audio data for which the audio editing apparatus 44 requests reproduction via the control interface, performs a parity check, and outputs the audio data to the audio editing apparatus 44.

Further, the disk array controller 20 has a function of detecting situations (obstacle) where trouble or the like would make recording and reproduction of the audio

data by the hard disk drives 220a and 220b impossible and after detecting that an obstacle is caused in the hard disk drive 220a, performs the recording and reproduction of the audio data by using the hard disk drive 220b. That is, the hard disk drives 220a and 220b of the audio data recording and reproducing apparatuses 2a to 2d form a complete duplex configuration.

First, an explanation will be made of the operation when recording the audio data in the audio data recording and reproducing apparatuses 2a to 2d in the audio editing system 4.

The audio editing apparatus 44 controls the audio data recording and reproducing apparatuses 2a to 2d via the control interface and makes them start the recording operation. Further, the audio editing apparatus 44 sequentially outputs for example the data blocks 0, 1, ..., 15 of the audio data for which the editing has been performed via the SCSI circuits 40a to 40d and 42a to 42d to the audio data recording and reproducing apparatuses 2a to 2d.

The disk array controller 20 of the audio data recording and reproducing apparatuses 2a to 2d make the individual hard disk drives 220a and 220b sequentially record the data blocks 0, 1, ..., 15 of the input audio data as shown in Fig. 4.

Next, an explanation will be made of the operation when reproducing the audio data from the audio data recording and reproducing apparatuses 2a to 2d in the audio editing system 4.

5 The audio editing apparatus 44 controls the audio data recording and reproducing apparatuses 2a to 2d via the control interface and makes them start for example the reproduction operation of the audio data.

10 The disk array controller 20 of the audio data recording and reproducing apparatuses 2a to 2d control the hard disk drive 220a, makes this sequentially reproduce the data blocks of the audio data, and outputs the same to the audio editing apparatus 44 via the SCSI circuits 40a to 40d and 42a to 42d.

15 Next, an explanation will be made of the operation of the audio data recording and reproducing apparatuses 2a to 2d where an obstacle occurs in one of the hard disk drives 220a and 220b.

20 Where it is detected that an obstacle has occurred in the hard disk drive 220a, the disk array controller 20 displays that the obstacle is caused in the hard disk drive 220a on for example a display device of the audio editing apparatus 44 or a display use lamp provided in the audio data recording and reproducing apparatuses 2a
25 to 2d (both are not illustrated) and, at the same time,

makes the hard disk drive 220b perform the reproduction of the audio data for which the reproduction is requested from the audio editing apparatus 44 after the detection of the obstacle of the hard disk drive 220a and outputs the reproduced data blocks to the audio editing apparatus 44.

When it is detected that the obstacle is caused in the hard disk drive 220b, the disk array controller 20 displays that the obstacle is caused in the hard disk drive 220b. Note, after the detection of the obstacle in the hard disk drive 220b, the hard disk drive 220a is made to perform the reproduction of the audio data requested from the audio editing apparatus 44 as it is and outputs the reproduced audio data to the audio editing apparatus 44.

As explained above, in the audio editing system 4, the audio editing apparatus 44 performs the recording and reproduction of the audio data with respect to the hard disk array 12 of the audio data recording and reproducing apparatuses 2a to 2d having a mirror configuration in which two hard disk drives 220a and 220b are connected in parallel. That is, the audio editing system 4 performs the reproduction of the audio data by using a recording apparatus adopting a complete duplex configuration, therefore has a high reliability and thus is suited to

the purpose of continuously reproducing the audio data of a program for broadcast in for example a television station or radio station.

Further, the editor can randomly access the hard
5 disk array 12 by using the audio editing apparatus 44,
reproduce any desired part of the audio data, and use the
same for the editing and therefore can perform the
editing work with a high efficiency.

Further, the audio editing apparatus 44 can transfer
10 a plurality of audio data in parallel with the audio data
recording and reproducing apparatuses 2a to 2d and
therefore can produce a plurality of channels worth of
the audio data in parallel.

Note that, the audio editing system 4 can be
15 modified by for example replacing the hard disk drive by
a magneto-optical disk device or the like in the same way
as the audio and/or video editing system 3 shown in the
first embodiment.

Further, it is also possible to, for example,
20 replace the audio editing apparatus 44 by a video editing
apparatus or an audio and/or video editing apparatus,
further replace the hard disk drives 220a and 220b by a
hard disk array and thereby to increase the recording
capacity, and make other modifications and apply the
25 audio editing system 4 for the editing of audio and/or

video data.

Third embodiment

Below, a third embodiment of the present invention will be explained.

5 First, an explanation will be made of a problem of the audio and/or video editing system 3 shown in the first embodiment of the present invention and the audio editing system 4 shown in the second embodiment for indicating the background by which the third embodiment
10 of the present invention was reached.

Figure 5 is a view of an access timing when the hard disk array 12 reproduces the audio and/or video data from the hard disk drives 120a to 120i in the audio and/or video editing system 3 (Fig. 1) shown in the first
15 embodiment.

In the audio and/or video editing system 3 shown in the first embodiment, the audio and/or video editing apparatus 34 and the audio and/or video data recording and reproducing apparatus 1 are connected via the SCSI
20 circuits 30 and 32, therefore the transfer rate of the audio and/or video data between them is limited by the data transfer capability of the SCSI circuits 30 and 32. At present, the transfer rate of the SCSI circuits 30 and 32 is about 160 Mbps, therefore, in actuality, the number
25 of channels of the audio and/or video data which can be

transferred in parallel between the audio and/or video editing apparatus 34 and the audio and/or video data recording and reproducing apparatus 1 becomes five or less ($\approx 160 \text{ Mbps}/30 \text{ Mbps}$).

5 Further, for example, when the audio and/or video editing apparatus 34 tries to reproduce four channels worth of the audio and/or video data in parallel from the audio and/or video data recording and reproducing apparatus 1, in order to supply the four channels (CH1 to
10 CH4) worth of audio and/or video data to the audio and/or video editing apparatus 34, the audio and/or video data recording and reproducing apparatus 1 must reproduce the four channels worth of the audio and/or video data in a time division manner. Accordingly, as shown in Fig. 5,
15 whenever the hard disk drives 120a to 120i (HDD0 to HDD8) access a different channel of the audio and/or video data, a seek and rotation waiting time are produced, the access time becomes intermittent, and in addition the amount of the audio and/or video data read out at one
20 time becomes small. That is, the time in which the hard disk drives 120a to 120i substantially access the four channels worth of audio and/or video data becomes shorter than that of the case where they access one channel worth of the audio and/or video data.

25 Further, where requests for recording and

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reproducing a plurality of channels worth of the audio and/or video data simultaneously occur, a discontinuity is caused in the audio and/or video data recorded and reproduced by the hard disk drives 120a to 120i, the recording and reproduction of the audio and/or video data in real time becomes difficult, and the continuous recording and reproduction of the audio and/or video data of a moving picture becomes difficult.

In order to deal with such a problem, there is also a method for controlling the SCSI circuits 30 and 32 so as to use the plurality of channels of audio and/or video data in a time division manner by dividing a predetermined cycle into a plurality of time slots.

However, when considering the possibility that the hard disk drives 120a to 120i will access the audio and/or video data a plurality of times in each of the time slots allocated to the plurality of channels, it is necessary to take measures to extend the length of the time slot or raise the speed of the hard disk drives 120a to 120i in advance. When taking the former measure, the cycle of finishing the round of reproduction of all of the channels of the audio and/or video data becomes long and the response of the reproduction operation of each channel of the audio and/or video data is lowered. On the other hand, when adopting the latter measure, there

arises a necessity of making the storage capacity of the buffer memory for buffering each channel of the audio and/or video data for every time slot large, which leads to a rise of costs of the audio and/or video editing system.

Here, where the audio editing system 4 (Fig. 3) shown in the second embodiment is modified as explained above and used for the purpose of performing the editing for a plurality of channels of the audio and/or video data in parallel, the seek time and rotation waiting time are independently produced in each of the hard disk drives 220a and 220b of the audio data recording and reproducing apparatuses 2a to 2d. Accordingly, if the configuration of the audio editing system 4 is adopted, the problem caused in the audio and/or video editing system 3 shown in the first embodiment can be solved.

However, the audio editing device 44 and the audio data recording and reproducing apparatuses 2a to 2d are connected by general SCSI circuits 40a to 40d and 42a to 42d, therefore a communication interface circuit, connection cable, and communication software (control interface) necessary for the communication of control data between the audio editing device 44 and the audio data recording and reproducing apparatuses 2a to 2d become separately necessary for the two.

That is, in the audio editing system 4, twice the number of the audio data recording and reproducing apparatuses 2a to 2d of the control interfaces becomes necessary in total, which becomes a cause preventing the reduction of size and lowering of costs of the audio editing system 4.

For the reduction of size and lowering of costs of the audio editing system 4, in the audio editing system 4, the measure of connecting the audio data recording and reproducing apparatuses 2a to 2d in the form of a daisy chain and reducing the control interfaces of the audio editing apparatus 44 to only one system can be considered. However, when constituting the audio editing system 4 in this way, the control processing between the audio editing apparatus 44 and the audio data recording and reproducing apparatuses 2a to 2d becomes complex, and the configuration of the audio data recording and reproducing apparatuses 2a to 2d cannot be simplified more than this.

The third embodiment of the present invention was made so as to solve the problem of the audio and/or video editing system 3 (Fig. 1) and the audio editing system 4 (Fig. 3) explained above and has as an object thereof to provide an audio and/or video data recording and reproducing apparatus capable of recording and

reproducing a plurality of channels worth of the audio and/or video data with a high efficiency.

Further, another object of the third embodiment of the present invention is to provide an audio and/or video data recording and reproducing apparatus in which the control processing between the audio and/or video editing apparatus and the audio and/or video data recording and reproducing apparatus is simplified and in addition the configuration of the apparatus is simple.

Figure 6 is a view of the configuration of an audio and/or video editing system 6 in the third embodiment.

As shown in Fig. 6, the audio and/or video editing system 6 is constituted by an audio and/or video data recording and reproducing apparatus 5, an audio and/or video editing apparatus 64, parallel/serial conversion circuits (P/S circuits) 620 and 624, and serial/parallel conversion circuits (S/P circuits) 622 and 626.

Figure 7 is a schematic view of the configuration and operation of the audio and/or video data recording and reproducing apparatus 5 shown in Fig. 6; and Fig. 8 is a view of a detailed configuration of the audio and/or video data recording and reproducing apparatus 5 shown in Fig. 6.

As shown in Fig. 7, the audio and/or video data recording and reproducing apparatus 5 is constituted by a

disk array controller 50 and a hard disk array 52 similar to the audio and/or video data recording and reproducing apparatus 1 (Fig. 2). The hard disk array 52 is constituted by eight hard disk drives 522a to 522h.

5 Further, the hard disk drives 522a and 522b constitute the disk device 520a of a mirror configuration similar to the disk device 22 (Fig. 4), the hard disk drives 522c and 522d constitute a disk device 520c of a mirror configuration, the hard disk drives 522e and 522f
10 constitute a disk device 520e of a mirror configuration, and the hard disk drives 522g and 522h constitute a disk device 520g of a mirror configuration.

Further, as shown in Fig. 8, the disk array controller 50 is constituted by SPC (SCSI protocol
15 controller) circuits 524a to 524h, data multiplexers 526a, 526c, 526e, and 526g, a main data multiplexer 528, a microprocessor (CPU), a control unit 530 comprising a memory circuit and peripheral circuits of the same, a status use FIFO 532, a read use data controller 534, a
20 command use FIFO 536, and a write use data controller 538.

In the audio and/or video editing system 6 (Fig. 6), the audio and/or video editing apparatus 64 and the audio and/or video data recording and reproducing apparatus 5
25 are connected via a transmission path for serially

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transmitting a data stream with the same transmission rate (270 Mbps) as that of for example the SDI (SMPTE-259M) via the P/S circuits 620 and 624 and S/P circuits 622 and 626. Note that, it is also possible to constitute the P/S circuits 620 and 624 and S/P circuits 622 and 626 by using the parallel/serial conversion semiconductor device and serial/parallel conversion semiconductor device developed for the SDI system, respectively.

Figure 9 is a view of the configuration of the data stream transmitted between the audio and/or video editing apparatus 64 and the audio and/or video data recording and reproducing apparatus 5 in the audio and/or video editing system 6 shown in Fig. 7.

Between the audio and/or video editing apparatus 64 and the audio and/or video data recording and reproducing apparatus 5, for example, as shown in Fig. 9, a data stream in which the synchronization data (SYNC), the control data (command data) used for controlling the audio and/or video data recording and reproducing apparatus 5 by the audio and/or video editing apparatus 64, or the status data used for indicating the state etc. of the apparatus by the audio and/or video data recording and reproducing apparatus 5 with respect to the audio and/or video editing apparatus 64, and the plurality of channels (4 channels (CH1, CH2, CH3 and CH4) of audio

and/or video data corresponding to the number of the disk devices 520a, 520c, 520e, and 520g in Fig. 9) are multiplexed is transmitted.

5 The audio and/or video editing apparatus 64 controls the audio and/or video data recording and reproducing apparatus 5, makes this reproduce the audio and/or video data, receives the same, performs the editing, outputs the audio and/or video data after the editing to the audio and/or video data recording and reproducing
10 apparatus 5, and makes this record the audio and/or video data.

In the audio and/or video data recording and reproducing apparatus 5 (Fig. 7 and Fig. 8), the disk array controller 50 records the data blocks 0, 1, ... of
15 the audio and/or video data input from the audio and/or video editing apparatus 64 in the individual hard disk drives 522a to 522h of the disk devices 520a, 520c, 520e, and 520g in an overlapped manner as shown in Fig. 7 under the control of the audio and/or video editing apparatus
20 64.

Further, where no obstacle occurs in the hard disk drives 520a, 520c, 520e, and 520g, the disk array controller 50 makes the hard disk drives 522a, 522c, 522e, and 522g of the hard disk drives 520a, 520c, 520e, and 520g reproduce the audio and/or video data for which
25

the reproduction is requested by the audio and/or video editing apparatus 64 and outputs the same to the audio and/or video editing apparatus 64.

Further, where an obstacle occurs in the disk devices 520a, 520c, 520e, and 520g, the disk array controller 50 displays this by transmitting the status data indicating the occurrence of the obstacle to the audio and/or video editing apparatus 64 similar to the disk array controller 20 (Fig. 4) of the audio data recording and reproducing apparatuses 2a to 2d and, at the same time, makes one of the two hard disk drives of each of the disk devices 520a, 520c, 520e, and 520g, in which the obstacle is not generated reproduce the audio and/or video data for which the reproduction is requested by the audio and/or video editing apparatus 64, and outputs the same to the audio and/or video editing apparatus 64.

In the audio and/or video data recording and reproducing apparatus 5 (Fig. 8), the write use data controller 538 demultiplexes the command data from the data stream by using the synchronization data contained in the data stream (Fig. 9) input from the audio and/or video editing apparatus 64 via the S/P circuit 626, outputs the same to the control unit 530 via the command use FIFO 536, demultiplexes each channel of the audio

and/or video data, and outputs the same to the main data multiplexer 528.

The read use data controller 534 operates in synchronization with the reception timing of the synchronization data of the data stream (Fig. 9) input from the audio and/or video editing apparatus 64, multiplexes each channel of the audio and/or video data input from the main data multiplexer 528 and status data input from the control unit 530 via the status use FIFO 532 on the data stream shown in Fig. 9, adds the synchronization data, and outputs the same to the audio and/or video editing apparatus 64 via the P/S circuit 620.

The control unit 530 detects the status of each constituent part of the audio and/or video data recording and reproducing apparatus 5 (existence of obstacle etc.) and generates the status data, outputs the same to the data controller 534 via the status use FIFO 532, and controls the constituent parts of the audio and/or video data recording and reproducing apparatus 5 based on the command data input from the data controller 538 via the command use FIFO 536 and status of the constituent parts of the audio and/or video data recording and reproducing apparatus 5 detected.

The main data multiplexer 528 operates in

synchronization with the reception timing of the
synchronization data of the data stream (Fig. 9) input
from the audio and/or video editing apparatus 64 and
outputs each of the first channel to the fourth channel
of the audio and/or video data input via the data
controller 538 to each of the data multiplexers 526a,
526c, 526e, and 526g. Further, the main data multiplexer
528 outputs the first channel to the fourth channel of
the audio and/or video data input from the data
multiplexers 526a, 526c, 526e, and 526g to the data
controller 534.

The data multiplexers 526a, 526c, 526e, and 526g
operate in synchronization with the reception timing of
the synchronization data of the data stream (Fig. 9)
input from the audio and/or video editing apparatus 64
and respectively record the first channel to the fourth
channel of the audio and/or video data input from the
main data multiplexer 528 in the hard disk drives 522a to
522h of the disk devices 520a, 520c, 520e, and 520g via
respectively corresponding SCSI circuits 524a to 524h in
an overlapped manner as mentioned above. Further, the
data multiplexers 526a, 526c, 526e, and 526g receive the
first channel to the fourth channel of the audio and/or
video data reproduced by the hard disk drives 522a to
522h of the corresponding disk devices 520a, 520c, 520e,

The SPC circuits 524a to 524h perform the SCSI
5 protocol control with the hard disk drives 522a to 522h
and input and output the audio and/or video data between
the data multiplexers 526a, 526c, 526e, and 526g and the
hard disk drives 522a to 522h.

First, an explanation will be made of the operation
when the audio and/or video data are recorded in the
audio and/or video data recording and reproducing
apparatus 5 at the audio and/or video editing system 6.

The audio and/or video editing apparatus 64
multiplexes the command data indicating the start of the
recording operation with respect to the audio and/or

video data recording and reproducing apparatus 5 in the data stream shown in Fig. 9 and controls the audio and/or video data recording and reproducing apparatus 5 to make this start the recording operation. Further, the audio and/or video editing apparatus 64 multiplexes the data blocks 0, 1, ..., 15 of the first channel to the fourth channel of the audio and/or video data in the data stream and outputs the same to the audio and/or video data recording and reproducing apparatus 5.

The disk array controller 50 of the audio and/or video data recording and reproducing apparatus 5 sequentially distributes the input data blocks 0, 4, ..., 12 of the first channel of the audio and/or video data to the disk device 520a as shown in Fig. 7, distributes the data blocks 1, 5, ..., 13 of the second channel of the audio and/or video data to the disk device 520b, distributes the data blocks 2, 6, ..., 14 of the third channel of the audio and/or video data to the disk device 520c, distributes the data blocks 3, 7, ..., 15 of the fourth channel of the audio and/or video data to the disk device 520d, and makes the hard disk drives 522a to 522h of the disk devices 520a, 520c, 520e, and 520g record the same in an overlapped manner.

That is, the disk device 520a records the first channel of the audio and/or video data in the hard disk

drives 522a and 522b in an overlapped manner, the disk device 520c records the second channel of the audio and/or video data in the hard disk drives 522c and 522d in an overlapped manner, the disk device 520e records the third channel of the audio and/or video data in the hard disk drives 522e and 522f in an overlapped manner, and the disk device 520g records the fourth channel of the audio and/or video data in the hard disk drives 522g and 522h in an overlapped manner.

Next, an explanation will be made of the operation when the audio and/or video data are reproduced from the audio and/or video data recording and reproducing apparatus 5 in the audio and/or video editing system 6.

The audio and/or video editing apparatus 64 multiplexes the command data for making the audio and/or video data recording and reproducing apparatus 5 start the reproduction operation and for designating the audio and/or video data to be reproduced in the data stream shown in Fig. 9 and controls the audio and/or video data recording and reproducing apparatus 5 to make this start the reproduction operation.

The disk array controller 50 of the audio and/or video data recording and reproducing apparatus 5 selects the disk device recording the channel of the audio and/or video data for which the reproduction is requested,

changes the hard disk drive to be made to perform the reproduction operation in accordance with the existence of an obstacle similar to the audio editing system 4 (Fig. 3), reproduces the audio and/or video data for which the reproduction is requested by the audio and/or video editing apparatus 64, multiplexes the same with the status data generated by the control unit 530 to generate the data stream (Fig. 9), and outputs this to the audio and/or video editing apparatus 64.

By constituting the audio and/or video editing system 6 as shown in the third embodiment, the audio and/or video data and the control use data can be multiplexed on one cable and transmitted between the audio and/or video editing apparatus 64 and the audio and/or video data recording and reproducing apparatus 5. Accordingly, unlike the audio editing system 4 (Fig. 3), it is not necessary to provide the control interface for each of the disk devices of the mirror configuration, and therefore the simplification of the control processing, reduction of size of apparatus, and the lowering of costs are possible.

Further, the main data multiplexer 528 of the audio and/or video data recording and reproducing apparatus 5 and hard disk drives 522a to 522h etc. operate in synchronization with the data stream input from the audio

and/or video editing apparatus 64, therefore the timing of recording and reproduction between the hard disk drives 522a to 522h synchronize. Accordingly, it is easy to bring the timings among a plurality of channels of audio and/or video data transmitted with the audio and/or video editing apparatus 64 into coincidence. Accordingly, the audio and/or video editing system 6 is preferred for the editing of a plurality of channels of the audio and/or video data and can improve the efficiency of the editing work of the editor using the audio and/or video editing system 6.

Further, the audio and/or video editing system 6 is constituted so that a plurality of disk devices having a mirror configuration are connected in parallel, and each of the plurality of channels of audio and/or video data is recorded in each of these disk devices.

Accordingly, unlike the audio and/or video data recording and reproducing apparatus 1 (Fig. 2) of the audio and/or video editing system 3 (Fig. 1), as shown in Fig. 10, in the audio and/or video data recording and reproducing apparatus 5, the access operation of each hard disk drive is independent.

That is, as shown in Fig. 10A to Fig. 10D, in the audio and/or video data recording and reproducing apparatus 5, when one channel of the audio and/or video

data is recorded and reproduced, only one disk device performs the seek and rotation waiting with respect to the hard disk, therefore the seek time and the rotation waiting time become short in comparison with those of the hard disk drives 120a to 120i of the audio and/or video data recording and reproducing apparatus 1, and the efficiency of access with respect to the audio and/or video data is better. Further, in the audio and/or video editing system 6, the audio and/or video editing apparatus 64 and the audio and/or video data recording and reproducing apparatus 5 are connected by the transmission path of the same transmission data rate (270 Mbps) as that by the SDI system, and therefore the number of channels of audio and/or video data can be increased in comparison with the audio and/or video data recording and reproducing apparatus 1 connected by the SCSI system.

Further, similar to the audio data recording and reproducing apparatuses 2a to 2d (Fig. 4), the same data is recorded in two hard disk drives in an overlapped manner, therefore the reliability is high. That is, the audio and/or video data recording and reproducing apparatus 5 overcomes the problems of the audio and/or video data recording and reproducing apparatus 1 and audio data recording and reproducing apparatuses 2a to 2d while having their advantages.

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Note that, the transmission system used between the audio and/or video editing apparatus 64 and the audio and/or video data recording and reproducing apparatus 5 may be another system having equivalent performances to those of the system shown in the third embodiment.

Further, the configuration of the data stream transmitted between the audio and/or video editing apparatus 64 and the audio and/or video data recording and reproducing apparatus 5 is not limited to that shown in Fig. 9. For example, other data (title of the audio and/or video data, data of time length, etc.) can be further multiplexed on the data stream too.

Further, modifications similar to those with respect to the audio and/or video editing system 3 and the audio editing system 4 are possible also with respect to the audio and/or video editing system 6.

Fourth embodiment

Below, a fourth embodiment of the present invention will be explained.

In the fourth embodiment, an explanation will be made of an audio and/or video editing system 8 having a configuration of a plurality of audio and/or video data recording and reproducing apparatuses 5 shown in the third embodiment connected in parallel where a plurality of transmission paths of the SDI system can be

accommodated.

Figure 11 is a view of the configuration of the audio and/or video editing system 8 according to the fourth embodiment of the present invention.

5 As shown in Fig. 11, the audio and/or video editing system 8 is constituted by an audio and/or video editing apparatus 84, a VTR apparatus 86, an operation desk 88, and six audio and/or video data recording and reproducing apparatuses 5a to 5f having the same configuration as
10 that of the audio and/or video data recording and reproducing apparatus 5 (Fig. 7 and Fig. 8) corresponding to transmission paths (for example six paths) of the SDI system connected to the audio and/or video editing system 8. Between the audio and/or video data recording and
15 reproducing apparatuses 5a to 5f and the audio and/or video editing apparatus 84, similar to for example the audio and/or video editing system 6 (Fig. 6), the data stream shown in Fig. 9 is transmitted via a transmission path of the same transmission rate (270 Mbps) as that of
20 the SDI system.

 The audio and/or video editing apparatus 84 is constituted by a VTR interface 846, video data controllers 844a to 844f, SDI interfaces 842a to 842f, and an interface unit 852. The interface unit 852 is
25 constituted by serial interfaces 854a to 854f

respectively corresponding to the audio and/or video data recording and reproducing apparatuses 5a to 5f and containing the P/S circuit 620 and the S/P circuit 626 (Fig. 8).

5 In the audio and/or video editing system 8, the SDI interfaces 842a to 842f respectively receive the data streams input from the transmission paths of the SDI system (SDI CH1 to CH6), demultiplex the audio and/or video data contained in the received data streams, and
10 output the same to the video data controllers 844a to 844f. Further, the SDI interfaces (SDI IF) 842a to 842f respectively multiplex the audio and/or video data input from the video data controllers 844a to 844f on the data stream of the SDI system and transmit the same to the
15 transmission paths CH1 to CH6 of the SDI system.

 The operation desk 88 accepts the operation data indicating the contents of the editing which should be executed by for example the audio and/or video editing apparatus 84 and outputs the same to the audio and/or
20 video editing apparatus 84.

 The video data controllers 844a to 844f respectively generate the command data used for controlling the audio and/or video data recording and reproducing apparatuses 5a to 5f in accordance with the operation data input from
25 the operation desk 88 by the editor and output the same

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to the audio and/or video data recording and reproducing apparatuses 5a to 5f via the serial interfaces 854a to 854f of the interface unit 852.

Further, the video data controllers 844a to 844f respectively perform the processings in accordance with the status data input from the audio and/or video data recording and reproducing apparatuses 5a to 5f. Further, the video data controllers 844a to 844f respectively control the input and output of the audio and/or video data among the transmission paths CH1 to CH6 of the SDI system, audio and/or video data recording and reproducing apparatuses 5a to 5f and the VTR interface 846.

The serial interfaces 854a to 854f of the interface unit 852 respectively input and output the data streams (Fig. 9) containing the command data or status data and the audio and/or video data between the audio and/or video data recording and reproducing apparatuses 5a to 5f and the video data controllers 844a to 844f.

The VTR interface (VTR IF) 846 controls the input and output of the audio and/or video data between the VTR apparatus 86 and the SDI interfaces 842a to 842f.

By constituting the audio and/or video editing apparatus 8 as explained above, the audio and/or video data supplied from the plurality of transmission paths CH1 to CH6 of SDI system and VTR apparatus 86 can be

recorded in the audio and/or video data recording and reproducing apparatuses 5a to 5f, respectively.

Further, it is possible to edit the audio and/or video data recorded in the audio and/or video data recording and reproducing apparatuses 5a to 5f in the audio and/or video editing apparatus 84 and record the audio and/or video data obtained as the result of the editing in the recording and reproducing apparatuses 5a to 5f and the VTR apparatus 86.

Further, the audio and/or video data recorded in the audio and/or video data recording and reproducing apparatuses 5a to 5f can be respectively transmitted to the transmission paths CH1 to CH6 of the SDI system.

Further, the audio and/or video data recording and reproducing apparatuses 5a to 5f perform the parallel operation in synchronization with the reception timing of the synchronization data of the data stream (Fig. 9) input from the audio and/or video editing apparatus 84, therefore the control of the timing of the audio and/or video data among them is not necessary. Accordingly, the storage capacity of the buffer memory for matching the timing of the audio and/or video data with the audio and/or video data recording and reproducing apparatuses 5a to 5f can be very small.

Further, each of the audio and/or video data

recording and reproducing apparatuses 5a to 5f has the hard disk array as shown in Fig. 7 and Fig. 8, therefore the storage capacity of the entire audio and/or video editing system 8 is very large. Accordingly, the audio and/or video editing system 8 has a similar characteristic feature to that of the audio and/or video editing system 6 (Fig. 6), and in addition is more preferred for the editing of complex audio and/or video data or editing of the audio and/or video data of a long program in comparison with the audio and/or video editing system 6.

As mentioned above, the audio and/or video data recording and reproducing apparatus according to the present invention and the method of same are preferred for editing since the audio and/or video data are recorded in a recording apparatus capable of random access and any desired part of the recorded audio and/or video data can be reproduced.

Further, according to the audio and/or video data recording and reproducing apparatus according to the present invention and the method of same, the audio and/or video data can be recorded and reproduced with a high efficiency by making the access time when recording and reproducing the audio and/or video data with respect to the recording medium capable random access as short as

possible.

Further, according to the audio and/or video data recording and reproducing apparatus according to the present invention and the method of same, a plurality of apparatuses are connected and complex editing with respect to the audio and/or video data can be performed with a high efficiency, and in addition, the physical connection of cable connections etc. can be reduced even in a case where a plurality of apparatuses are connected.

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What is claimed is:

1. An audio and/or video data recording and reproducing apparatus having:

5 a receiving means for receiving a data stream in which a plurality of audio data and video data or one of the same (audio and/or video data) are multiplexed in a predetermined order;

10 a demultiplexing means for demultiplexing each of said audio and/or video data from the received said data stream;

a plurality of recording means for recording each of the demultiplexed said plurality of audio and/or video data so that random access is possible;

15 a reproducing means for reproducing said plurality of audio and/or video data respectively recorded in said plurality of recording means; and

a multiplexing means for multiplexing the reproduced said plurality of audio and/or video data in said predetermined order and generating said data stream.

20 2. An audio and/or video data recording and reproducing apparatus according to claim 1, wherein

each of said plurality of recording means adopts a mirror configuration having a plurality of recording apparatuses for recording the same audio and/or video data.

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3. An audio and/or video data recording and reproducing apparatus according to claim 1, wherein each of said plurality of recording means adopts an array configuration in which a plurality of recording apparatuses are connected in parallel.

4. An audio and/or video data recording and reproducing apparatus according to claim 1, wherein:

control data is further multiplexed on said data stream;

said demultiplexing means further demultiplexes said control data multiplexed on said data stream; and

provision is further made of a recording and reproduction control means for controlling a recording operation of said recording means and reproduction operation of said reproducing means based on the demultiplexed said control data.

5. An audio and/or video data recording and reproducing apparatus according to claim 4, wherein

at least one of said plurality of recording means and said plurality of reproducing means perform a recording operation and reproduction operation in synchronization with a synchronization signal of said data stream.

6. An audio and/or video data recording and reproducing apparatus according to claim 5, wherein

said plurality of audio and/or video data recording and reproducing apparatuses are connected in parallel, and said data stream is input and output among these plurality of audio and/or video data recording and reproducing apparatuses.

7. An audio and/or video data recording and reproduction method comprising steps of:

receiving a data stream in which a plurality of audio data and video data or one of the same (audio and/or video data) are multiplexed in a predetermined order;

demultiplexing each of said audio and/or video data from the received said data stream;

recording each of the demultiplexed said plurality of audio and/or video data so that random access is possible;

reproducing said plurality of audio and/or video data respectively recorded in said plurality of recording means; and

multiplexing the reproduced said plurality of audio and/or video data in said predetermined order and generating said data stream.

8. An audio recording and reproduction method according to claim 7, wherein

each of the demultiplexed said plurality of

audio and/or video data is recorded on a plurality of recording media to perform backup for them.

9. An audio recording and reproduction method according to claim 8, wherein

5 each of the demultiplexed said plurality of audio and/or video data is recorded on a plurality of recording media in parallel for every part.

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AUDIO AND/OR VIDEO DATA RECORDING
AND REPRODUCING APPARATUS AND METHOD OF SAME

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ABSTRACT OF THE DISCLOSURE

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An audio and/or video data recording and reproducing apparatus and method of the same to improve an efficiency of editing with respect to audio and/or video data. An audio and/or video editing apparatus 64 controls an audio and/or video data recording and reproducing apparatus 5 and makes this start the reproduction operation. A disk array controller 50 selects a disk device recording the channel of the audio and/or video data for which the reproduction is requested, changes the hard disk drive made to perform the reproduction operation in accordance with the existence of an obstacle, reproduces the audio and/or video data, multiplexes the same with status data indicating the state of parts of the audio and/or video data recording and reproducing apparatus 5, generates a data stream physically adapted to SMPTE-259M, and outputs this to an audio and/or video editing apparatus 64.

SONY-P7325

Declaration and Power of Attorney For Patent Application

特許出願宣言書

Japanese Language Declaration

私は、下欄に氏名を記載した発明者として、以下のとおり宣言する：

私の住所、郵便の宛先および国籍は、下欄に氏名に隣りて記載したとおりであり、

名称の発明に関し、請求の範囲に記載した特許を求める主題の本来の、最初にして唯一の発明者である（一人の氏名のみが下欄に記載されている場合）か、もしくは本来の、最初にして共同の発明者である（複数の氏名が下欄に記載されている場合）と信じ、

その明証書を
(該当する方に印を付す)

☐ ここに添付する。

☐ _____ 日に出版番号

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(該当する場合)

私は、前記のとおり補正した請求の範囲を含む前記明証書の内容を検討し、理解したことを陳述する。

私は、連邦規則法典第37部第1章第56条 (a) 項に従い、本願の審査に所要の情報を開示すべき義務を有することを認める。

As a below named inventor, I hereby declare that:

My residence, post office address and citizenship are as stated below next to my name,

I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled

AUDIO AND/OR VIDEO DATA RECORDING

AND REPRODUCING APPARATUS AND

METHOD OF SAME

the specification of which

(check one)

☒ is attached hereto.

☐ was filed on _____ as

Application Serial No. _____

and was amended on _____
(if applicable)

I hereby state that I have reviewed and understand the contents of the above identified specification, including the claims, as amended by any amendment referred to above.

I acknowledge the duty to disclose all information which is material to patentability as defined in Title 37, Code of Federal Regulations, Section 1.56.

Japanese Language Declaration

私は、合衆国法典第35部第119条にもとづく下記の外国特許出願または発明者証出願の外国優先権利益を主張し、さらに優先権の主張に係わる基礎出願の出願日前の出願日を有する外国特許出願または発明者証出願を以下に明記する：

I hereby claim foreign priority benefits under Title 35, United States Code, §119 of any foreign application(s) for patent or inventor's certificate listed below and have also identified below any foreign application for patent or inventor's certificate having a filing date before that of the application on which priority is claimed:

Prior foreign applications
先の外国出願

Priority claimed
優先権の主張

P08-085102	Japan	8 April 1996
(Number)	(Country)	(Day/Month/Year Filed)
(番 号)	(国 名)	(出願の年月日)
_____	_____	_____
(Number)	(Country)	(Day/Month/Year Filed)
(番 号)	(国 名)	(出願の年月日)
_____	_____	_____
(Number)	(Country)	(Day/Month/Year Filed)
(番 号)	(国 名)	(出願の年月日)
_____	_____	_____

<input checked="" type="checkbox"/> Yes	<input type="checkbox"/> No
あり	なし
<input type="checkbox"/> Yes	<input type="checkbox"/> No
あり	なし
<input type="checkbox"/> Yes	<input type="checkbox"/> No
あり	なし

私は、合衆国法典第35部第120条にもとづく下記の合衆国特許出願の利益を主張し、本願の請求の範囲各項に記載の主題が合衆国法典第35部第112条第1項に規定の様態で先の合衆国出願に開示されていない限度において、先の出願の出願日と本願の国内出願日またはPCT国際出願日の間に公表された、連邦規則法典第37部第1章第56条に記載の、特許性に対し重要である全ての情報を米国特許商標庁に開示すべき義務を有することを認める：

I hereby claim the benefit under Title 35, United States Code, § 120 of any United States application(s) listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States application in the manner provided by the first paragraph of Title 35, United States Code, § 112, I acknowledge the duty to disclose information which is material to patentability as defined in Title 37, Code of Federal Regulations, § 1.56 which became available between the filing date of the prior application and the national or PCT international filing date of this application:

(Application Serial No.)	(Filing Date)
(出願番号)	(出願日)
_____	_____
(Application Serial No.)	(Filing Date)
(出願番号)	(出願日)
_____	_____

(現 況)	(Status)
(特許済み、係属中、放棄済み)	(patented, pending, abandoned)
_____	_____
(現 況)	(Status)
(特許済み、係属中、放棄済み)	(patented, pending, abandoned)
_____	_____

私は、ここに自己の知識にもとづいて行った陳述がすべて真実であり、自己の有する情報および信ずるところに従って行った陳述が真実であると信じ、さらに故意に虚偽の陳述等を行った場合、合衆国法典第18部第1001条により、罰金もしくは禁錮に処せられるか、またはこれらの刑が併科され、またかかる故意による虚偽の陳述が本願ないし本願に対して付与される特許の有効性を損うことがあることを認識して、以上の陳述を行ったことを宣言する。

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

Japanese Language Declaration

委任状：私は、下記発明者として、以下の代理人をここに選任し、本願の手続を遂行すること並びにこれに関する一切の行為を特許商標庁に対して行うことを委任する。
(代任人氏名および登録番号を明記のこと)

POWER OF ATTORNEY: As a named inventor, I hereby appoint the following attorney(s) and/or agent(s) to prosecute this application and transact all business in the Patent and Trademark Office connected therewith. (list name and registration number.)

I HEREBY APPOINT THE FOLLOWING AS MY ATTORNEYS WITH FULL POWER OF SUBSTITUTION TO PROSECUTE THIS APPLICATION AND TRANSACT ALL BUSINESS IN THE PATENT OFFICE CONNECTED THEREWITH:

Karl A. Limbach	18,689	Michael J. Pollock	29,098	David Woycechowsky	39,079
George C. Limbach	19,305	Stephen M. Everett	30,050	Alan S. Hodes	38,185
John K. Uilkema	20,282	Alfred A. Equitz	30,922	Patricia Coleman James	37,155
J. William Wigert, Jr.	24,582	W. Patrick Bengtsson	32,456	Alan A. Limbach	39,749
Philip M. Shaw, Jr.	25,376	Mark A. Della Valle	34,147	Douglas C. Limbach	35,249
Neil A. Smith	25,441	Charles P. Sammut	28,901	Brian J. Keating	39,520
Veronica C. Devitt	29,375	Richard A. Nebb	33,540	Michael R. Ward	38,651
Ronald L. Yin	27,607	Richard E. Wawrzyniak	36,048	Steven M. Santisi	40,157
Gerald T. Sekimura	30,103	Alan D. Minsk	35,956	J. Thomas McCarthy	22,420
Michael A. Stallman	29,444	Mark C. Pickering	36,239	Theodore Naccarella	33,023
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唯一のまたは第一の発明者の氏名	Full name of sole or first inventor
	YASUO IWASAKI
同発明者の署名	Inventor's signature
日付	Date
住所	Residence
	Kanagawa, Japan
国籍	Citizenship
	Japan
郵便の宛先	Post Office Address
	c/o SONY CORPORATION
	7-35, Kitashinagawa 6-chome,
	Shinagawa-ku, Tokyo, Japan
第2の共同発明者の氏名 (該当する場合)	Full name of second joint inventor, if any
同第2発明者の署名	Second inventor's signature
日付	Date
住所	Residence
国籍	Citizenship
郵便の宛先	Post Office Address

(第六またはそれ以降の共同発明者に対しても同様な情報および署名を提供すること。)

(Supply similar information and signature for third and subsequent joint inventors.)

FIG. 1

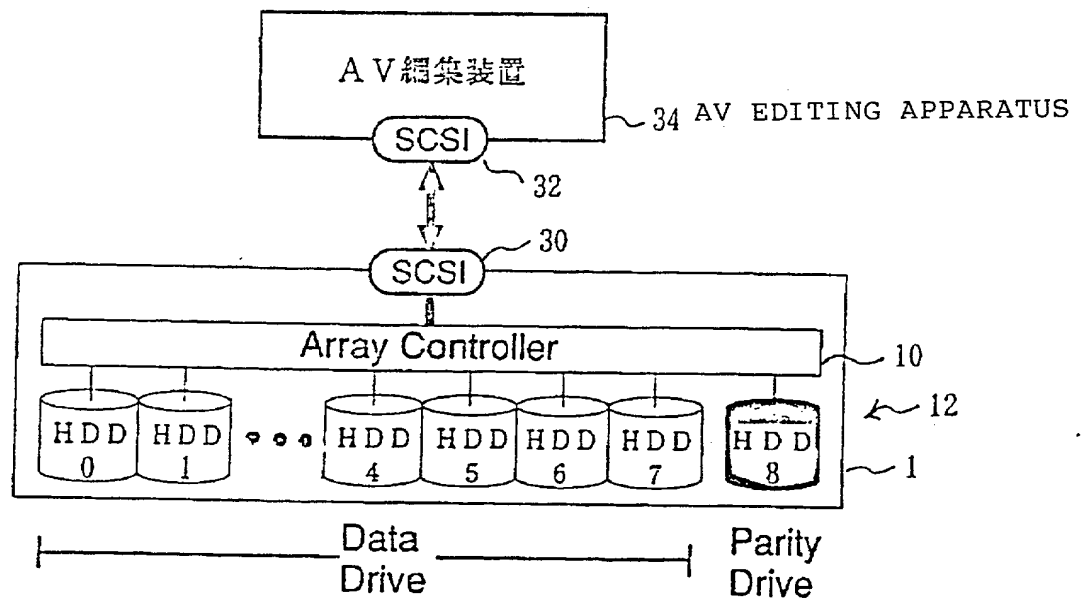


FIG. 2

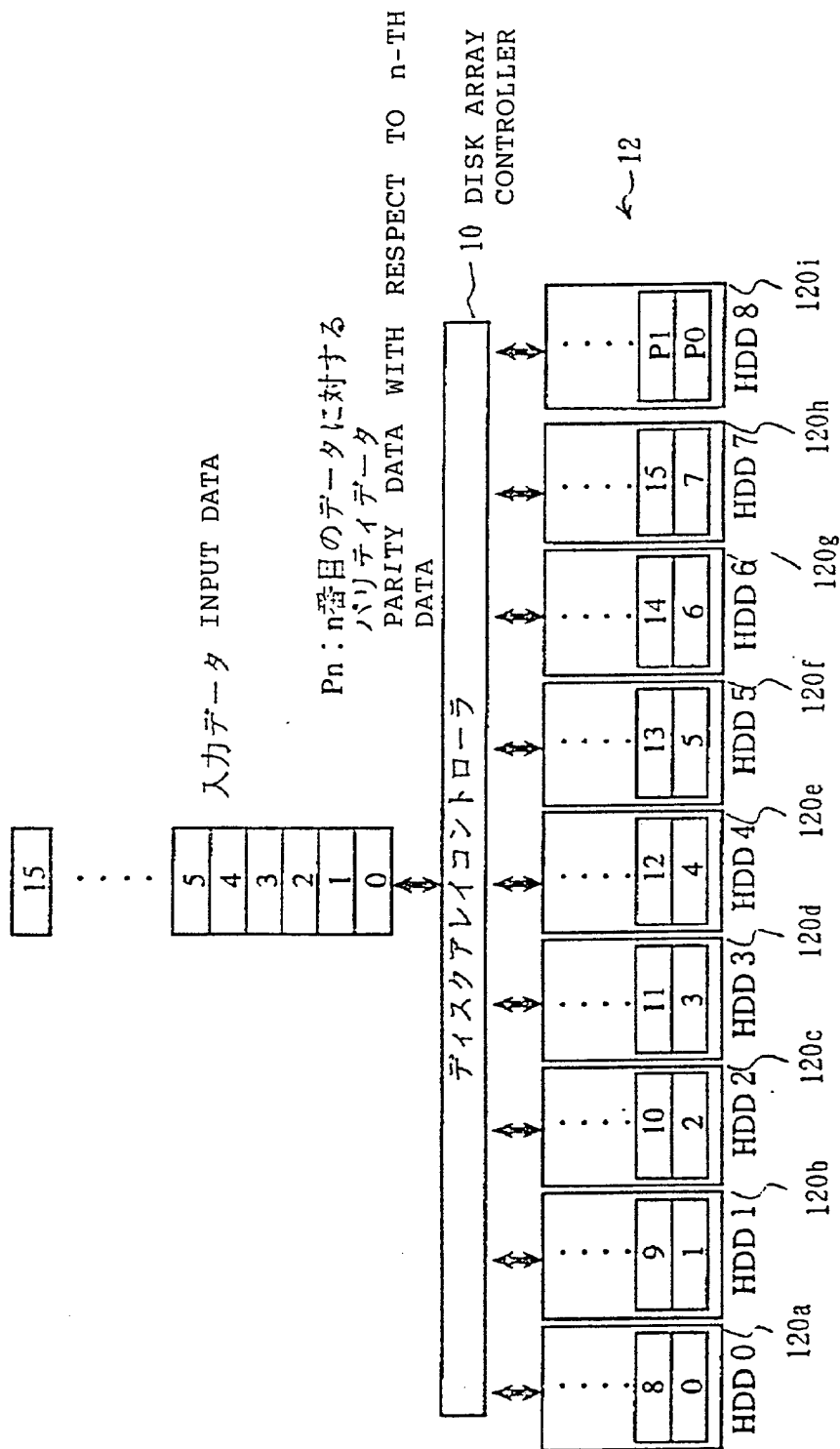
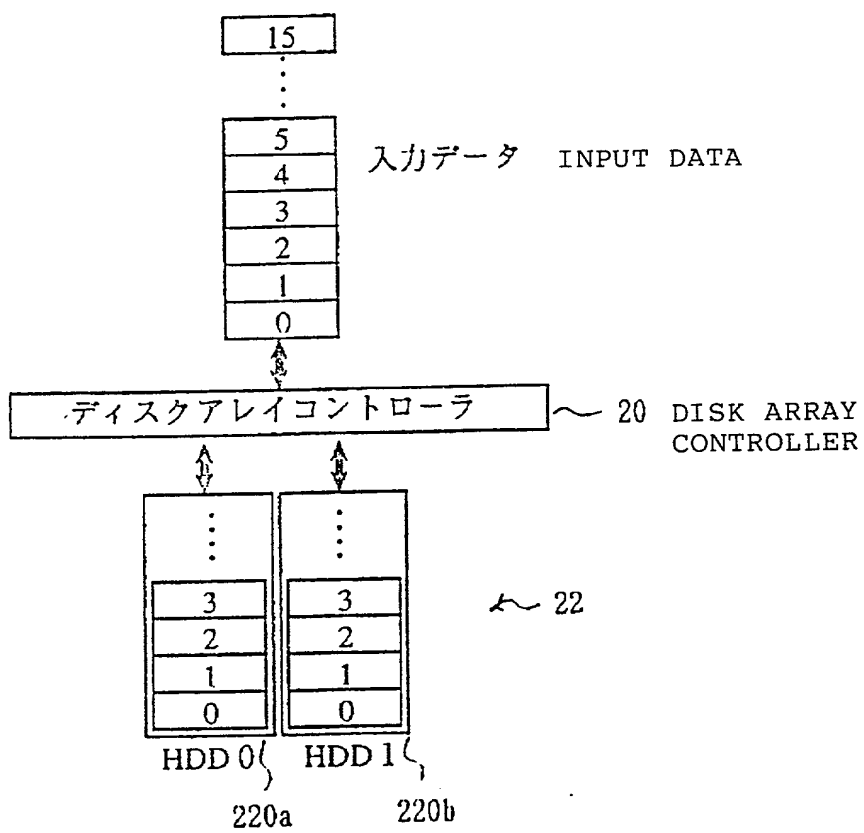


FIG. 4



2a~2d

08326744-040797

FIG. 6

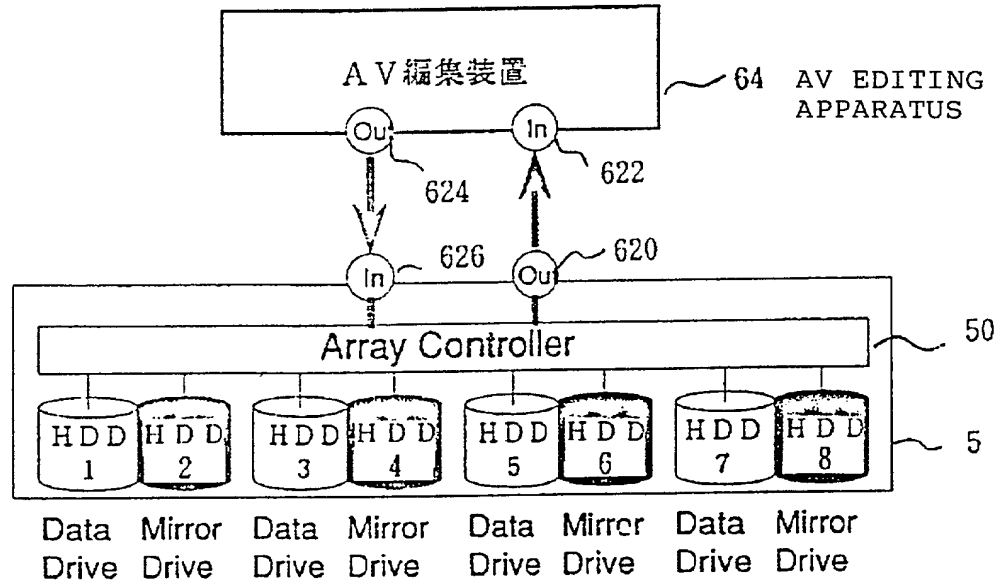


FIG. 7

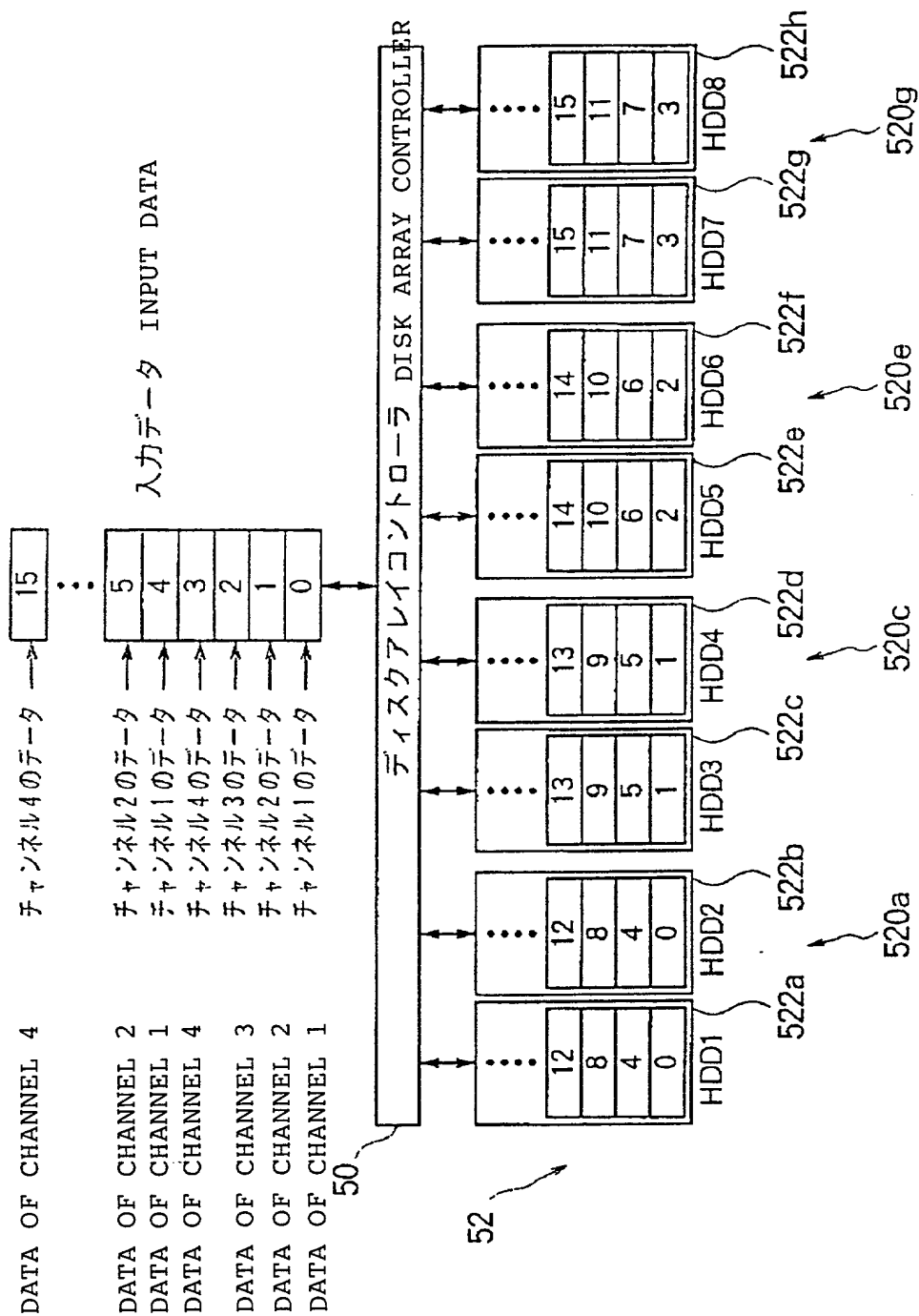


FIG. 8

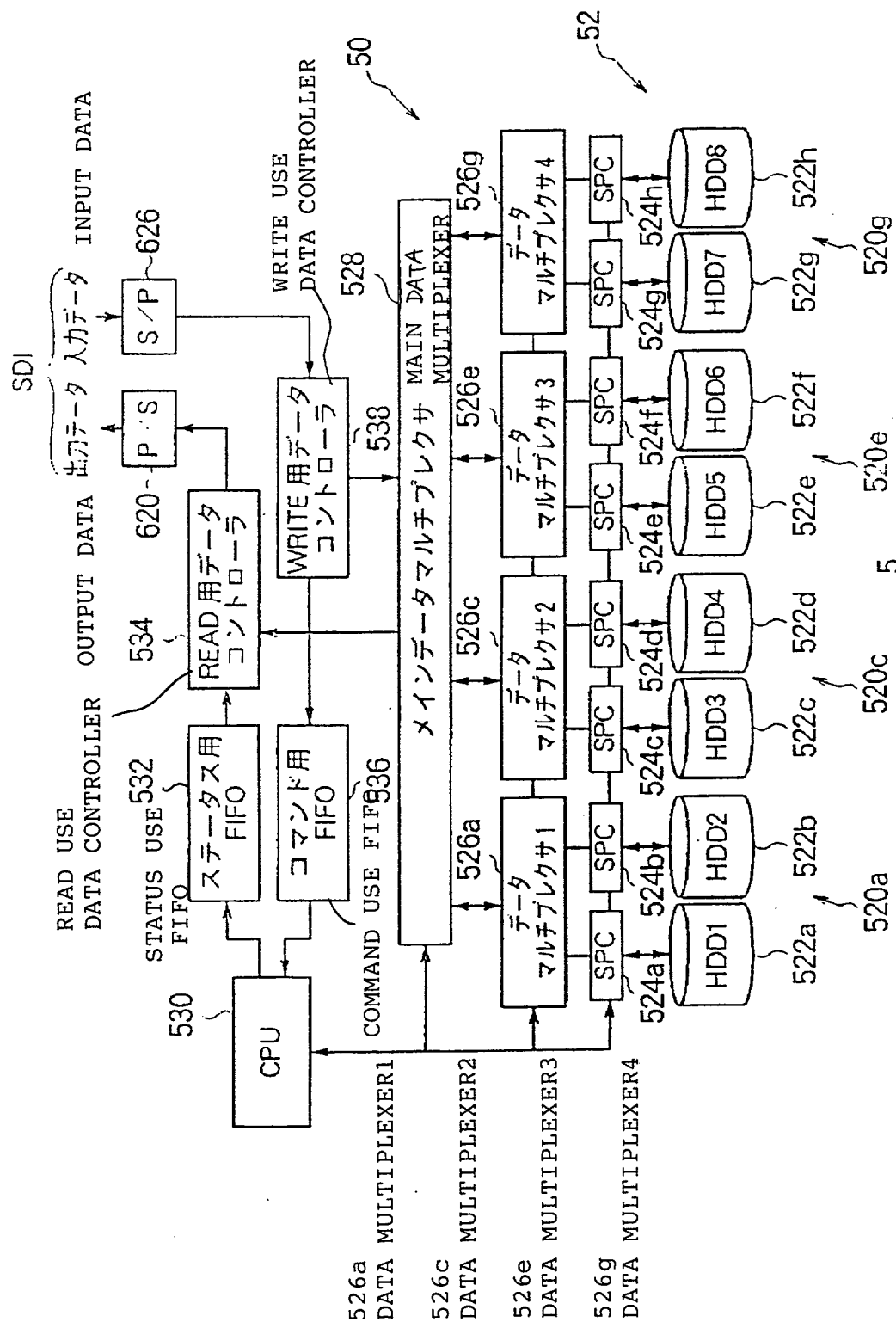


FIG. 9

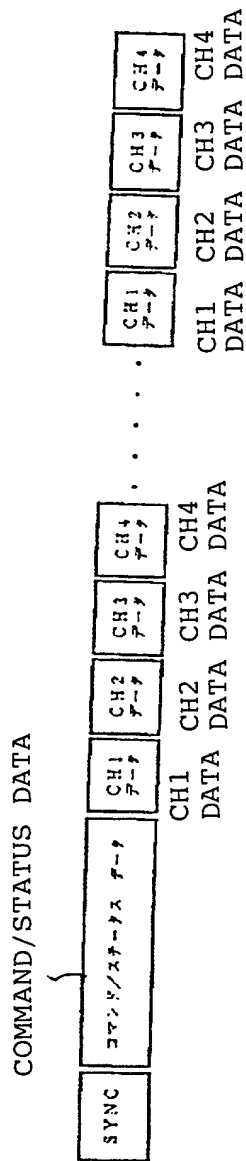


FIG. 10A

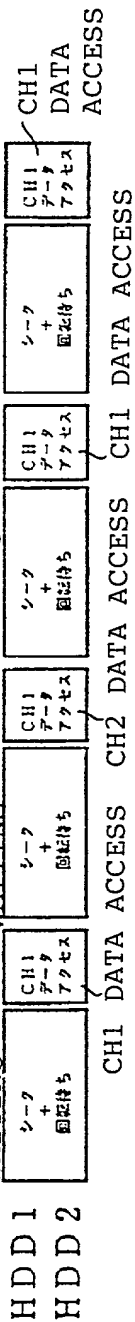


FIG. 10B

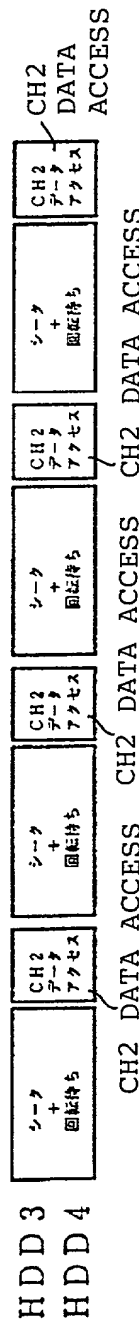


FIG. 10C

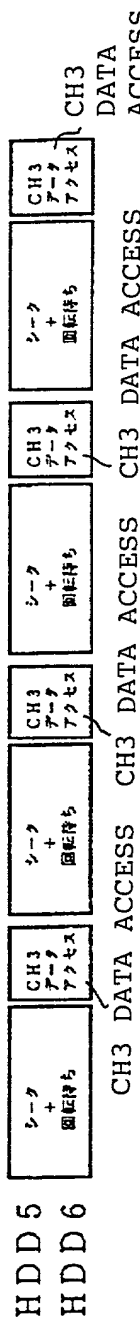


FIG. 10D

